

Refine Search

Search Results -

Terms	Documents
(status near5 request) same plural\$3 same (data adj1 bus)	21

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L1

Refine Search

Recall Text

Clear

Interrupt

Search History

 DATE: Friday, February 11, 2005 [Printable Copy](#) [Create Case](#)
Set Name Query

side by side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1 (status near5 request) same plural\$3 same (data adj1 bus)

Hit Count Set Name

result set

21 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(status near5 request) same plural\$3 same (data adj1 bus)	4

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L2

Refine Search

Recall Text

Clear

Interrupt

Search History

 DATE: Friday, February 11, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR
L2 (status near5 request) same plural\$3 same (data adj1 bus)

 4 L2
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR
L1 (status near5 request) same plural\$3 same (data adj1 bus)

 21 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(370/464 709/208 709/224 709/253 702/122 710/15 710/19 710/100 710/11 710/300 710/105 710/107 710/305 710/315 712/31 340/3.1 714/47).ccls.	11143

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L3

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Friday, February 11, 2005 [Printable Copy](#) [Create Case](#)

SetName Query

side by

side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L3 710/15,19,100,11,300,105,107,305,315;702/122;340/3.1;709/208,224,253;712/31;370/464;714/4

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L2 (status near5 request) same plural\$3 same (data adj1 bus)

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1 (status near5 request) same plural\$3 same (data adj1 bus)

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and L3	3

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L4



Refine Search

Recall Text



Clear

Interrupt

Search History

DATE: Friday, February 11, 2005 [Printable Copy](#) [Create Case](#)

Set
Name Query
 side by
 side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L4 l1 and L3

L3 710/15,19,100,11,300,105,107,305,315;702/122;340/3.1;709/208,224,253;712/31;370/464;714/4

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L2 (status near5 request) same plural\$3 same (data adj1 bus)

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1 (status near5 request) same plural\$3 same (data adj1 bus)

END OF SEARCH HISTORY

EAST - [Untitled1:1]

File
View
Edit
Tools
Window
Help

Drafts

Pending

Active

L1: (14) (status near5 request)

Failed

Saved

Favorites

Tagged (0)

UDC

Queue

Trash

Search

List

Browse

Create

Clear

DBs

USPAT

Default operator: OR

☒ Plurals
☒ Highlight all hit terms initially

BRS form

IS&R form

Image

Text

HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err
1	BRS	L1	14	(status near5 request) same plural\$3 same (data	USPAT	2005/02/11 10:26			

Start

EAST - [Untitled1:1]

Start EAST - [Untitled1]

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE



Membership | Publications/Services | Standards | Conferences | Careers/Jobs

IEEE Xplore®
 RELEASE 1.8

 Welcome
 United States Patent and Trademark Office

[Help](#) | [FAQ](#) | [Terms](#) | [IEEE Peer Review](#)
[Quick Links](#)

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced
- ☐ CrossRef

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet

Your search matched **0** of **1124699** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set
Results Key:**JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**Results:****No documents matched your query.**

Print Format

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE



Membership Publications/Services Standards Conferences Careers/Jobs

IEEE Xplore®
 RELEASE 1.8

 Welcome
 United States Patent and Trademark Office

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)

Quick Links

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced
- ☐ CrossRef

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet

Your search matched **1** of **1124699** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or entering a new one in the text box.

status and request and bus

Search☐ Check to search within this result set**Results Key:****JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**1 A fair distributed queue dual bus access method***Khalil, K.M.; Koblentz, M.E.;*

Local Computer Networks, 1989., Proceedings 14th Conference on , 10-12 Oct 1989

Pages:180 - 188

[\[Abstract\]](#) [\[PDF Full-Text \(564 KB\)\]](#) **IEEE CNF**

Print Format

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE



Membership Publications/Services Standards Conferences Careers/Jobs

IEEE Xplore®
 RELEASE 1.8

 Welcome
 United States Patent and Trademark Office

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)
[Quick Links](#)

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced
- ☐ CrossRef

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet

Print Format

[Search Results](#) [\[PDF FULL-TEXT 564 KB\]](#) [DOWNLOAD CITATION](#)

 Request Permissions
RIGHTSLINK

A fair distributed queue dual bus access method

Khalil, K.M. Koblentz, M.E.

Bell Commun. Res. Inc., Piscataway, NJ, USA;

This paper appears in: Local Computer Networks, 1989., Proceedings 14 Conference on

Meeting Date: 10/10/1989 - 10/12/1989

Publication Date: 10-12 Oct. 1989

Location: Minneapolis, MN USA

On page(s): 180 - 188

Reference Cited: 4

Inspec Accession Number: 3668104

Abstract:

The distributed queue dual **bus** (DQDB) has emerged as a proposed draft standard IEEE 802.6 Metropolitan Area Network Working Group. DQDB nodes using the arbitrated access mechanism are shown to access the transmission medium in a fair manner. The fundamental access property is analyzed in detail and simulation modeling results are shown that verify the analysis. A modified access control mechanism is proposed that augments the normal DQDB information about downstream node **status** information about upstream nodes, thereby allowing the distributed queue nodes to behave like a centralized queue with a round-robin queueing discipline. This is based on distributed control of the slot reservation **requests** in order to minimize the effects of propagation delay and allow multiple simultaneous entries in the distributed queue. For each DQDB priority level, this may be accomplished by the addition of a few bits in the access control field and two counters per **bus**. The proposed mechanism is shown to be robust in the presence of transmission errors in the access control field.

Index Terms:

computer interfaces computer networks performance evaluation protocols queueing standards telecommunication traffic IEEE 802.6 Metropolitan Area Network Working Group asymmetric access property bit transmission errors distributed queue dual bus downstream nodes modified access control mechanism multiple simultaneous entries priority level propagation delay proposed draft standard protocols queued arbitrated access mechanism round-robin queueing discipline simulation modeling results slot reservation requests nodes

Documents that cite this document

There are no citing documents available in IEEE Xplore at this time.

[Search Results](#) [\[PDF FULL-TEXT 564 KB\]](#) [DOWNLOAD CITATION](#)

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) |
[New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online](#)
[Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved

Hit List

[Clear](#)[Generate Collection](#)[Print](#)[Fwd Refs](#)[Bkwd Refs](#)[Generate OACS](#)

Search Results - Record(s) 1 through 3 of 3 returned.

☐ 1. Document ID: US 20040128416 A1

Using default format because multiple data bases are involved.

L4: Entry 1 of 3

File: PGPB

Jul 1, 2004

PGPUB-DOCUMENT-NUMBER: 20040128416

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040128416 A1

TITLE: Apparatus and method for address bus power control

PUBLICATION-DATE: July 1, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Kurts, Tsvika	Haifa		IL	
Orenstien, Doron	Haifa		IL	
Yuffe, Marcelo	Binyamina		IL	

US-CL-CURRENT: 710/107; 713/300

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw D
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	-----	--------

☐ 2. Document ID: US 20030149819 A1

L4: Entry 2 of 3

File: PGPB

Aug 7, 2003

PGPUB-DOCUMENT-NUMBER: 20030149819

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030149819 A1

TITLE: Method and apparatus for ascertaining the status of multiple devices simultaneously over a data bus

PUBLICATION-DATE: August 7, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Smith, Robert B.	Loveland	CO	US	
Cross, Ted	Fort Collins	CO	US	

US-CL-CURRENT: 710/100

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

☐ 3. Document ID: US 4523272 A

L4: Entry 3 of 3

File: USPT

Jun 11, 1985

US-PAT-NO: 4523272

DOCUMENT-IDENTIFIER: US 4523272 A

TITLE: Bus selection control in a data transmission apparatus for a multiprocessor system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
-------	---------------------	-------	----------	-----------	---------------

Terms	Documents
L1 and L3	3

Display Format:

[Previous Page](#) [Next Page](#) [Go to Doc#](#)

[First Hit](#) [Previous Doc](#) [Next Doc](#) [Go to Doc#](#)☐ [Generate Collection](#) [Print](#)

L4: Entry 2 of 3

File: PGPB

Aug 7, 2003

PGPUB-DOCUMENT-NUMBER: 20030149819
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030149819 A1

TITLE: Method and apparatus for ascertaining the status of multiple devices
simultaneously over a data bus

PUBLICATION-DATE: August 7, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Smith, Robert B.	Loveland	CO	US	
Cross, Ted	Fort Collins	CO	US	

APPL-NO: 10/ 068029 [PALM]
DATE FILED: February 6, 2002

INT-CL: [07] G06 F 13/00

US-CL-PUBLISHED: 710/100
US-CL-CURRENT: 710/100

REPRESENTATIVE-FIGURES: 1

ABSTRACT:

Techniques are provided for simultaneously ascertaining the status of a plurality of devices coupled to a data bus. A master device transmits at least one status request message over the data bus to a plurality of slave devices. In response, the plurality of slave devices transmit to the master device a status indicator message including a plurality of status indicators indicating statuses of the plurality of slave devices. The master device receives the status indicator message and ascertains the status of at least some of the plurality of slave devices by examining the status indicators. The status request message and/or status indicator message may be a message defined according to a protocol associated with the data bus. The data bus may, for example, be a serial data bus such as an I.sup.2C bus.

[Previous Doc](#) [Next Doc](#) [Go to Doc#](#)



US006535948B1

(12) United States Patent
Wheeler et al.

(10) Patent No.: **US 6,535,948 B1**
(45) Date of Patent: **Mar. 18, 2003**

(54) SERIAL INTERFACE UNIT

5,758,075 A • 5/1998 Liang et al. 710/105
5,850,268 A • 12/1998 Ohki et al. 348/535

(75) Inventors: Paul Kurt Wheeler, Allentown, PA
(US); Andrew Lawrence Webb,
Hamilton, NJ (US); William G.
Burroughs, Macungie, PA (US)

• cited by examiner

(73) Assignee: Agere Systems Inc., Allentown, PA
(US)

Primary Examiner—Peter Wong
Assistant Examiner—David Glass
(74) Attorney, Agent, or Firm—Ryan, Mason & Lewis, LLP

(*) Notice: Subject to any disclaimer, the term of this
patent is extended or adjusted under 35
U.S.C. 154(b) by 307 days.

(57) ABSTRACT

A serial interface unit having an input shift register adapted to receive a serial input data from a serial data stream, and a destination request module. The input shift register converting the serial input data into a parallel input data. The input shift register in communication with at least two processors and the destination request module. The destination request module in communication with one of the at least two processors in response to an input shift register status signal and a processor designation signal, the selected processor adapted to receive the parallel input data.

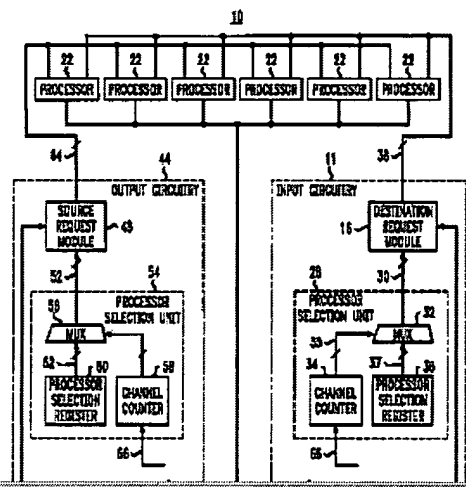
(21) Appl. No.: 09/583,493
(22) Filed: May 31, 2000
(51) Int. Cl. G06F 13/36
(52) U.S. Cl. 710/310
(58) Field of Search 710/105, 310

(56) References Cited

U.S. PATENT DOCUMENTS

5,426,784 A • 6/1995 Kewala et al. 365/219

42 Claims, 3 Drawing Sheets



US-PAT-NO: 4808992

DOCUMENT-IDENTIFIER: US 4808992 A

TITLE: Component audio/video system with automatic turn-off of peripheral devices

----- KWIC -----

Brief Summary Text - BSTX (8):

It has been recognized that a large number of audio/video and audio-only devices may be combined in a unified home entertainment system by providing a central control unit coupled via a digital data bus to individual device of the system. An example of such a system is described by B. W. Beyers, Jr. in U.S. Pat. No. 4,581,645 entitled DISTRIBUTED SWITCHED COMPONENT AUDIO/VIDEO SYSTEM. Other examples include U.S. Pat. No. 4,337,480 of Bourassin et al. entitled, DYNAMIC AUDIO-VIDEO INTERCONNECTION SYSTEM and U.S. Pat. No. 4,488,179 of Kruger et al. entitled TELEVISION VIEWING CENTER SYSTEM. R. A. Pitsch describes a bus-oriented system in an article entitled "Dimensia: The Next Dimension of Sight and Sound" published in RCA Engineer, July/August edition, 1985, at pp.66-70. In the described system the control bus provides two-way communication between a central controller and a plurality of individual device controllers. By this feature a user may transmit a status request and receive a response (displayed on the screen of his TV receiver) that gives the status (e.g., play, pause, etc.) of any selected device in his system. The user may also transmit commands via the two-way data bus to select and control the "status" or operating mode of individual audio and video peripheral devices connected to the bus.

United States Patent [19]
Beyers, Jr. et al.

[11] Patent Number: **4,808,992**
[45] Date of Patent: **Feb. 28, 1989**

- [54] **COMPONENT AUDIO/VIDEO SYSTEM WITH AUTOMATIC TURN-OFF OF PERIPHERAL DEVICES**
- [75] Inventors: Billy W. Beyers, Jr., Greenfield; James E. Hickey, Indianapolis, both of Ind.
- [73] Assignee: RCA Licensing Corporation, Princeton, N.J.
- [21] Appl. No.: 47,881
- [22] Filed: May 8, 1987
- [51] Int. Cl.⁴ H04Q 3/00
- [52] U.S. Cl. 340/825.24; 358/181; 358/333
- [58] Field of Search 340/825.06, 825.07, 340/825.22, 825.24, 825.25; 455/4, 5, 353; 358/181, 188, 189, 194.1, 335; 360/74.1, 33.1

[56] **References Cited**

U.S. PATENT DOCUMENTS

- | | | | |
|-----------|---------|-----------------|------------|
| 3,922,641 | 11/1975 | Quinn, Jr. | 340/825.5 |
| 4,379,012 | 7/1981 | Backus et al. | 340/825.22 |
| 4,357,480 | 6/1982 | Bouras et al. | 358/333 |
| 4,400,733 | 8/1983 | Schramm et al. | 358/181 |
| 4,418,333 | 11/1983 | Schramm et al. | 340/825.22 |
| 4,488,179 | 12/1984 | Kruger et al. | 358/181 |
| 4,327,204 | 7/1985 | Kozaki et al. | 360/74.1 |
| 4,381,643 | 4/1986 | Beyers, Jr. | 358/181 |
| 4,628,370 | 12/1986 | Pukacka | 360/74.1 |
| 4,631,601 | 12/1986 | Bugliari et al. | 358/333 |

4,649,428 3/1987 Jones et al. 358/194.1

FOREIGN PATENT DOCUMENTS

0125284 12/1984 European Pat. Off. 340/363 VI

OTHER PUBLICATIONS

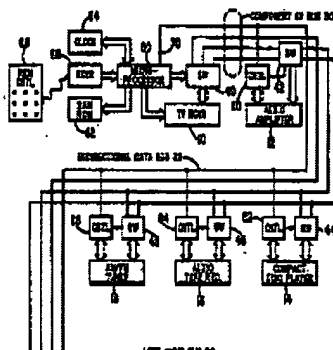
R. A. Fitch, "Dimenda, The Next Dimension of Sight and Sound," *RCA Engineer*, Jul/Aug. 1983, pp. 66-70.

Primary Examiner—John W. Caldwell, Sr.
Assistant Examiner—Edwin C. Holloway, III
Attorney, Agent, or Firm—Eugene M. Whitacre, Paul J. Reardon, Peter M. Emanuel

[57] **ABSTRACT**

A component audio/video system includes a system controller coupled to supply selection and control commands to a plurality of peripheral audio/video devices and to receive status messages therefrom. The devices may be selected to form a number of different recording configurations involving combinations of one or more recording signal sources and one or more devices for recording the signals. The system controller enters one of a number of different control modes for turning power off to the signal sources and recording devices depending on the status messages identifying recording configurations to enable continued recording by some devices when others run out of recording or playback media.

3 Claims, 2 Drawing Sheets



United States Patent [19]

Nadlr

[11] 4,257,095

[45] Mar. 17, 1981

[54] SYSTEM BUS ARBITRATION, CIRCUITRY AND METHODOLOGY

[73] Inventor: James Nadlr, Sunnyvale, Calif.

[73] Assignee: Intel Corporation, Santa Clara, Calif.

[21] Appl. No.: 931,083

[22] Filed: Jan. 30, 1978

[51] Int. Cl.³ G06F 3/00

[52] U.S. Cl. 364/200

[53] Field of Search 364/200, 300, 200 MS File, 364/900 MS File

[56] References Cited

U.S. PATENT DOCUMENTS

3,825,902	7/1974	Brown et al.	364/200
3,983,540	9/1976	Keller et al.	364/200
3,993,258	11/1976	Barlow	364/200
4,040,028	8/1977	Peiker et al.	364/200
4,148,011	4/1979	McLagan et al.	364/900 X
4,181,974	1/1980	Lenny et al.	364/900

Primary Examiner—James D. Thomas
Assistant Examiner—Thomas M. Heckler
Attorney Agent or Firm—Blakely, Sokoloff, Taylor & Zafman

[37] ABSTRACT

Arbitration of a system bus shared by a plurality of digital processors, input and output devices and memories may be shared in an intelligent and efficient manner by using an arbitration method and an arbiter and bus controller circuit which allows a lower priority processor or user to access the system bus during those times in which a higher priority user of the system bus is not actively accessing the system bus. Thus, without altering the priority assignments among multiple users of a system bus, lower priority users requesting access may be allowed selective and limited access to the system bus during those times in which a higher priority user is in either an idle or halt state or is engaged in utilizing another bus, such as an input/output bus or resident bus.

21 Claims, 20 Drawing Figures

